

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Trace-Impedance Matching at Junctions of Multi-Load Signal
Traces to Eliminate Termination

Application Number :

10/707249

Confirmation Number:

First Named Applicant:

Yao Yen

Attorney Docket Number:

PS-109

Art Unit:

Examiner:

Search string:

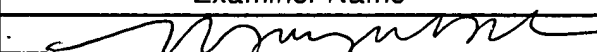
(6515555 or 6104629 or 6487086 or 6236572 or 6449166 or 6484299 or 6067594
or 5426405 or 6061263 or 5532954 or 5260892 or 6542393).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
DN	1	6515555	2003-02-04	Leddige et al.	B1	333	034
DN	2	6104629	2000-08-15	Wu	B1	365	063
DN	3	6487086	2002-11-26	Ikeda et al.	B1	361	772
DN	4	6236572	2002-05-22	Teshome et al.	B1	361	764
DN	5	6449166	2002-09-10	Sly et al.	B1	361	760
DN	6	6484299	2002-11-19	Larsen	B1	716	006
DN	7	6067594	2002-05-23	Perino et al.	B1	710	310
DN	8	5426405	1995-05-23	Miller et al.	B1	333	247
DN	9	6061263	2000-05-09	Boaz et al.	B1	365	051
DN	10	5532954	1996-07-02	Bechtolsheim et al.	B1	365	052
DN	11	5260892	1993-11-09	Testa et al.	B1	365	063
DN	12	6542393	2003-04-21	Chu et al.	B1	365	051

Signature

Examiner Name	Date
	11/1/04